

Figure 1

Coordination Interfaces

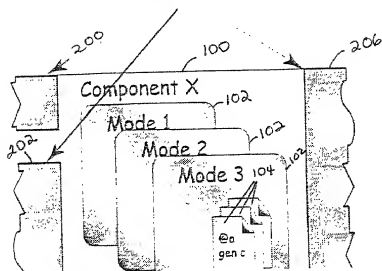


Figure 2

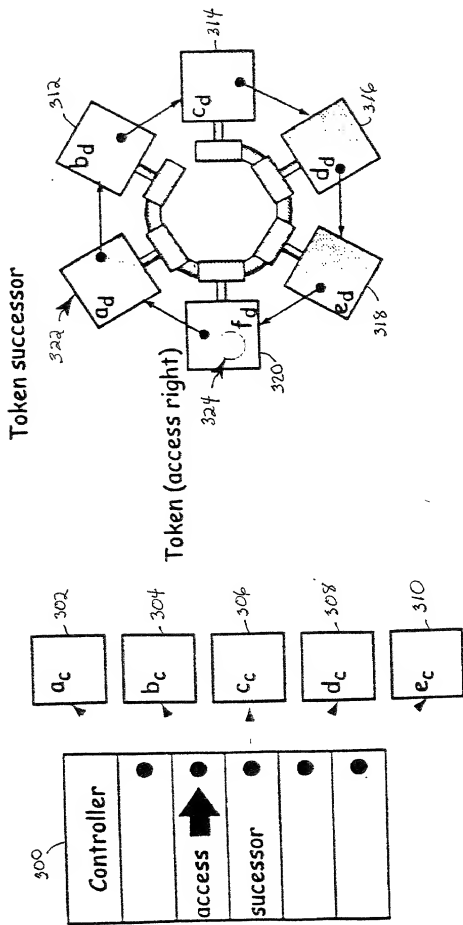


Figure 3B

Figure 3A

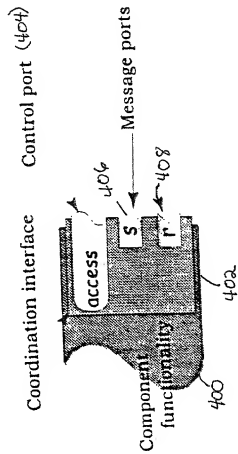


Figure 4A

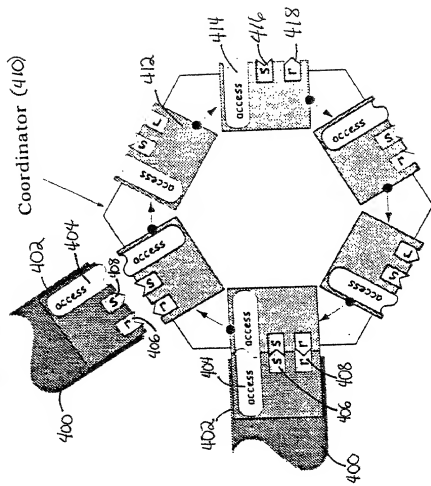


Figure 4B

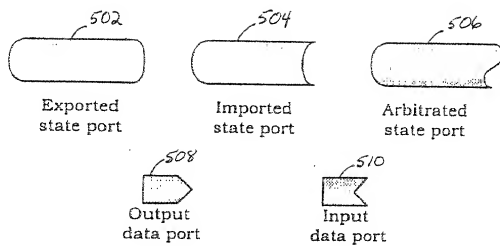


Figure 5

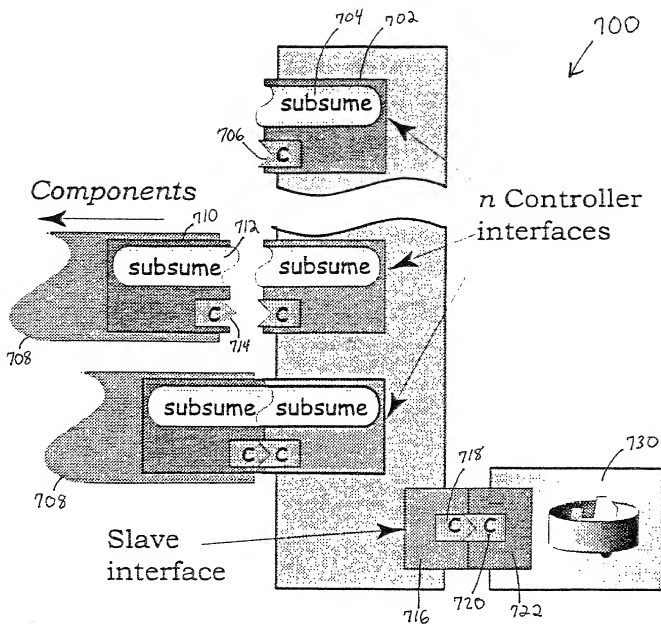


Figure 7

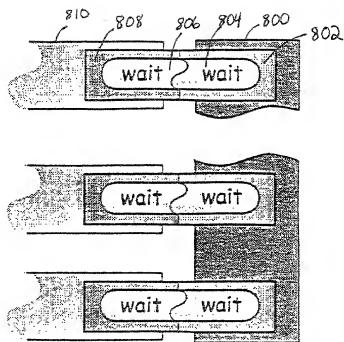


Figure 8

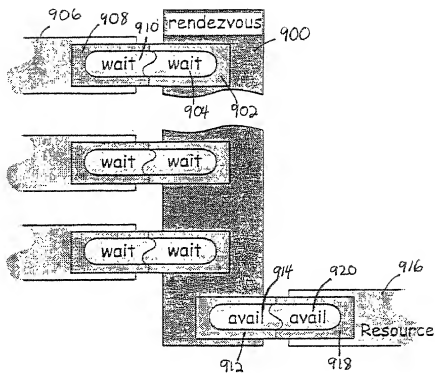


Figure 9

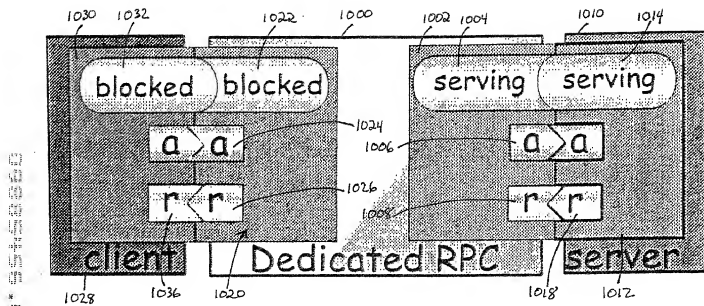


Figure 10

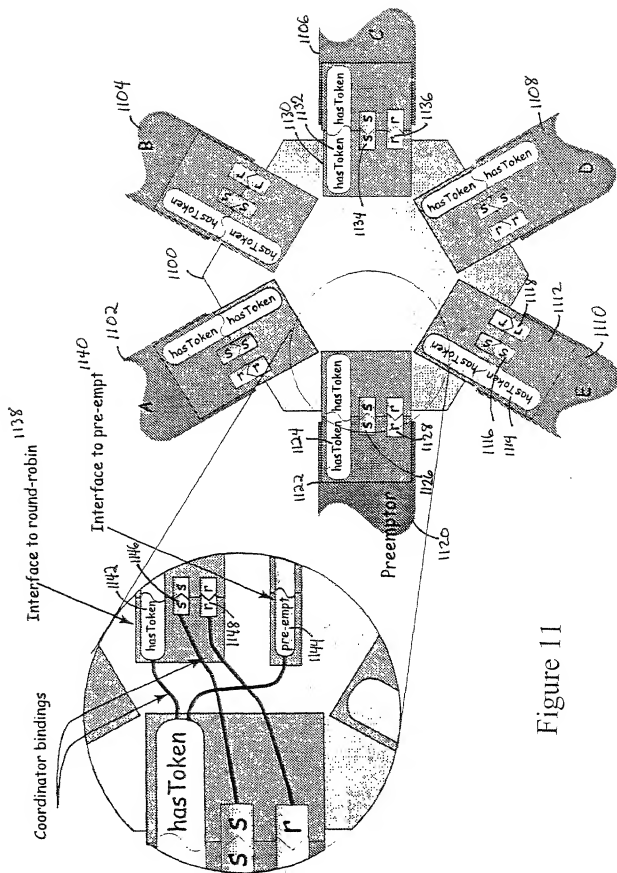


Figure 11

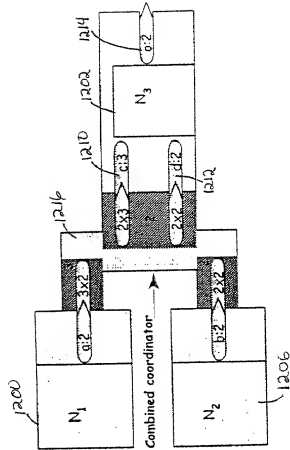


Figure 12B

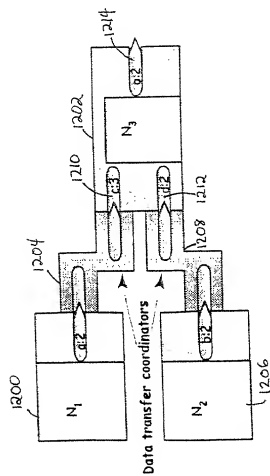


Figure 12A

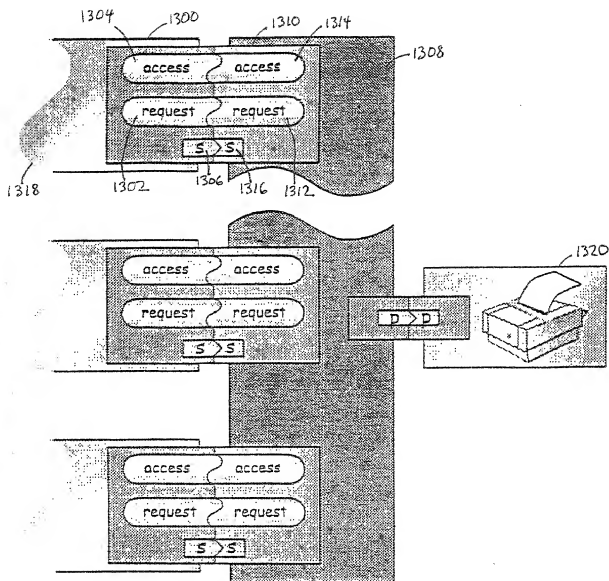


Figure 13

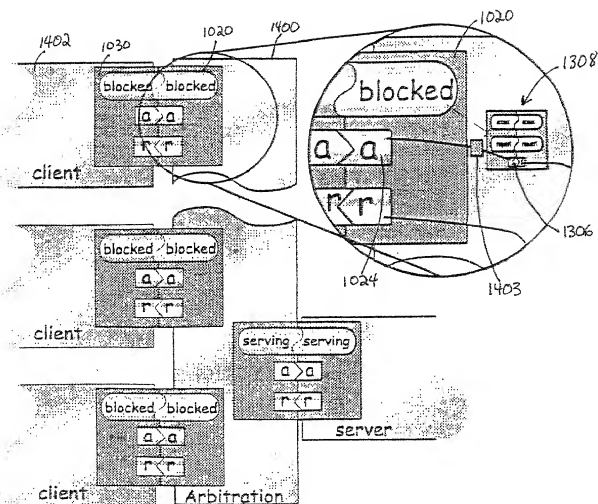


Figure 14

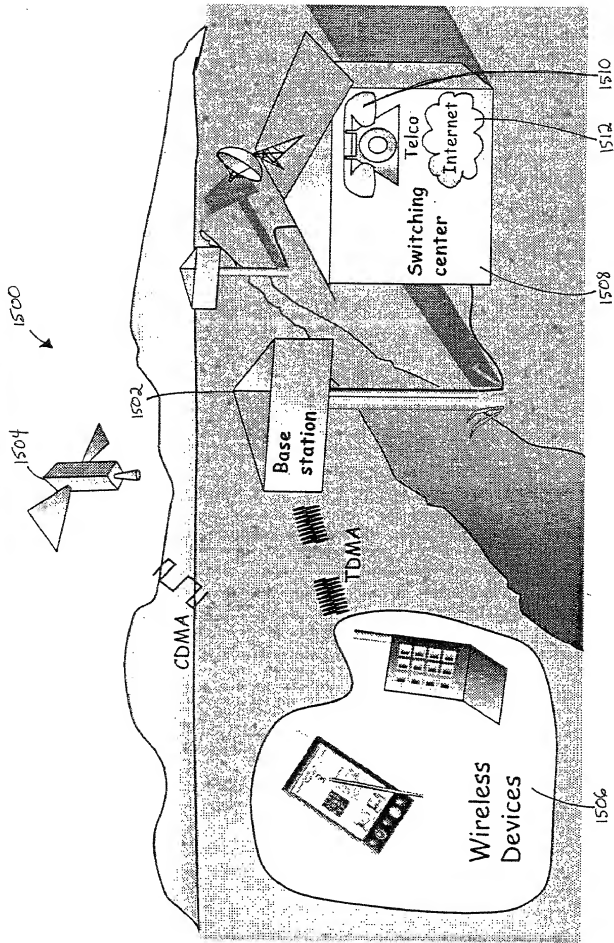


Figure 15

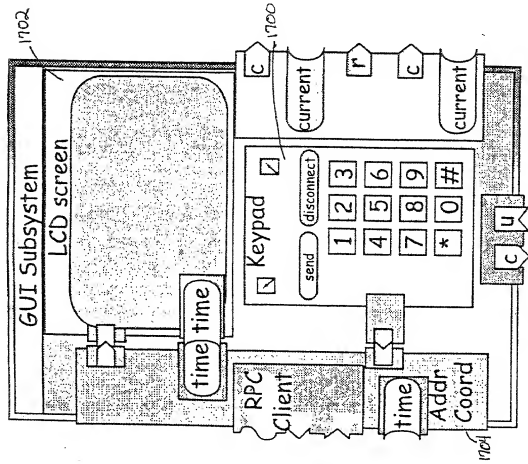


Figure 17A

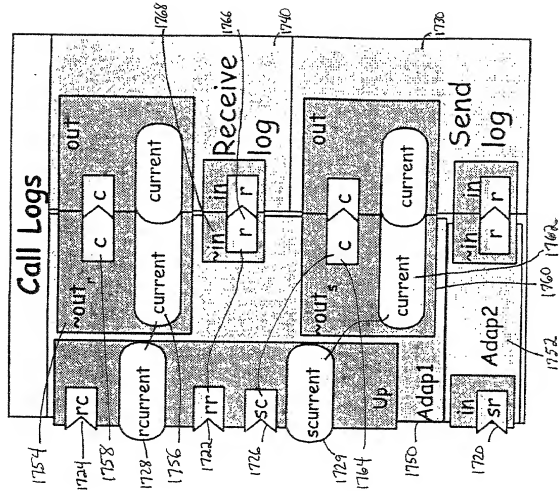


Figure 17B

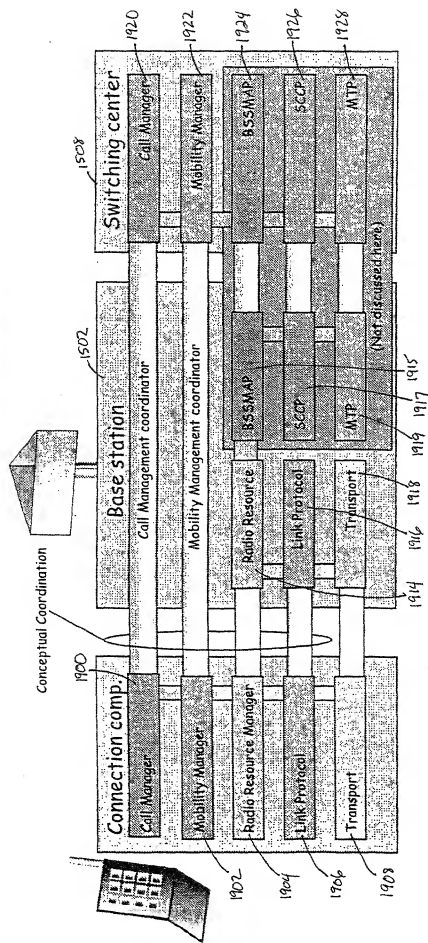


Figure 19

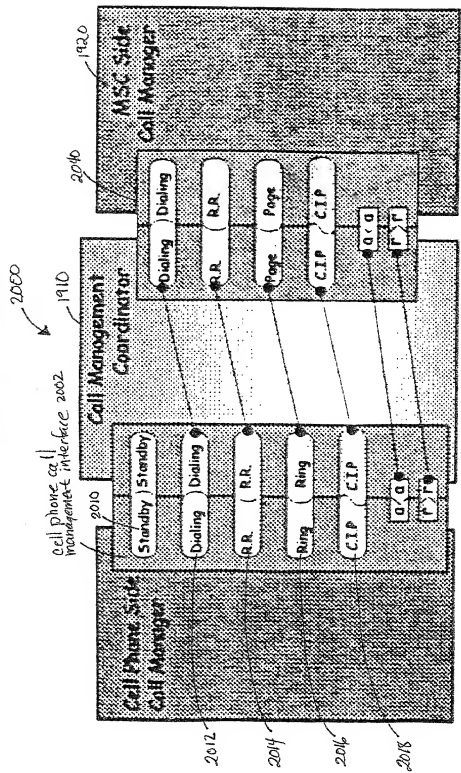


Figure 20

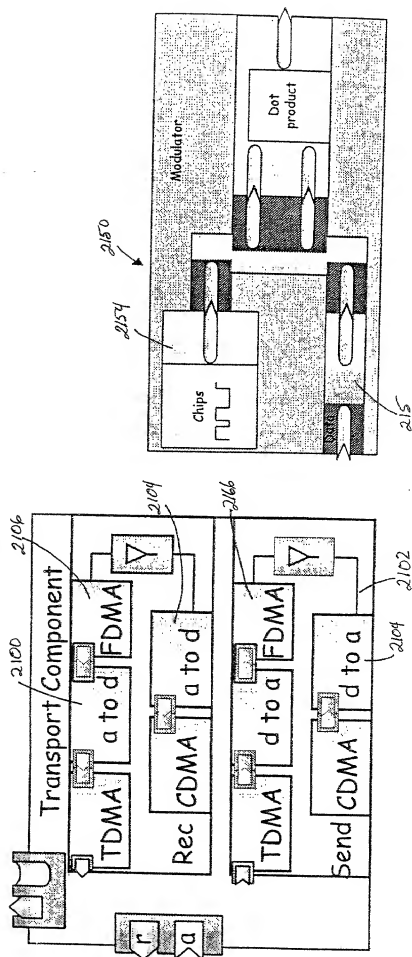


Figure 21A

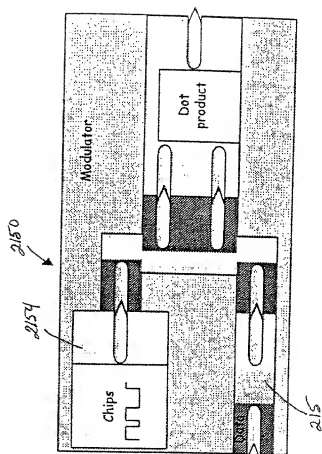


Figure 21B

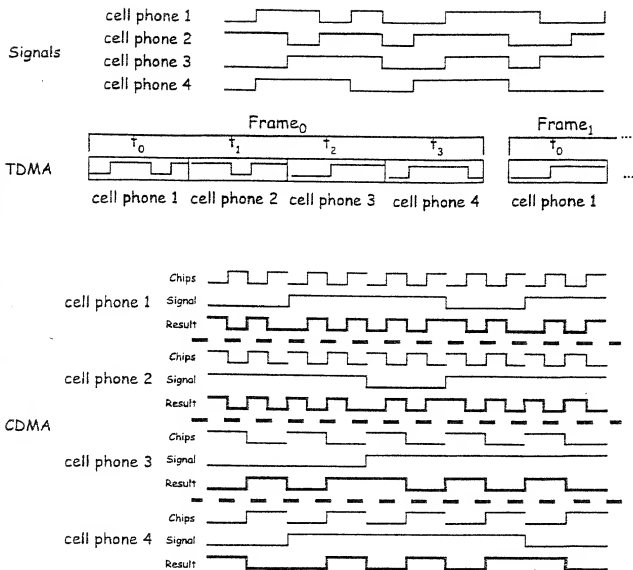


Figure 22

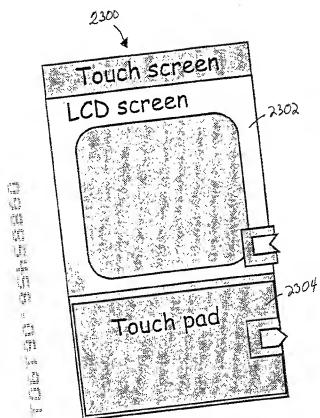


Figure 23A

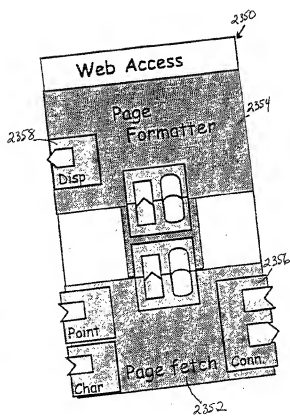


Figure 23B

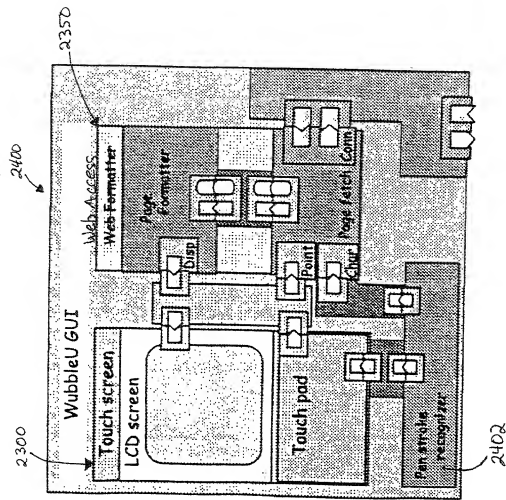


Figure 24A

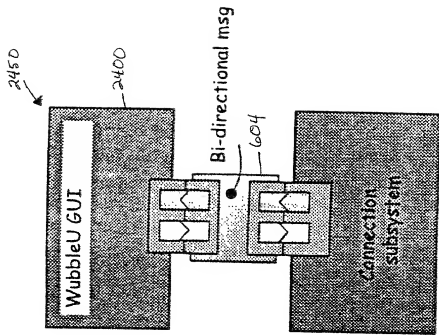


Figure 24B

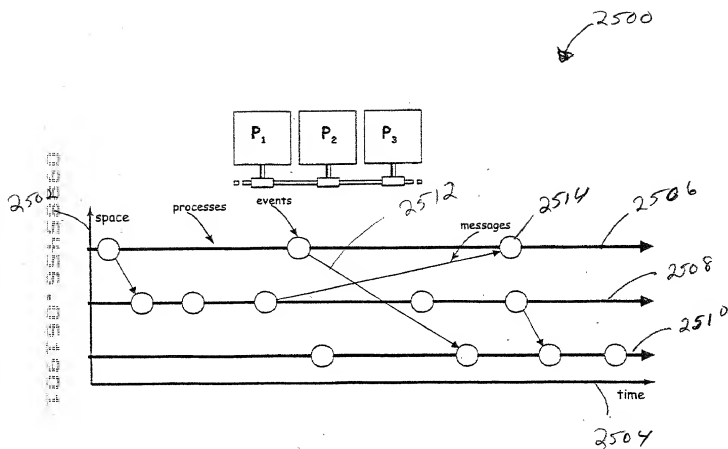


Figure 25

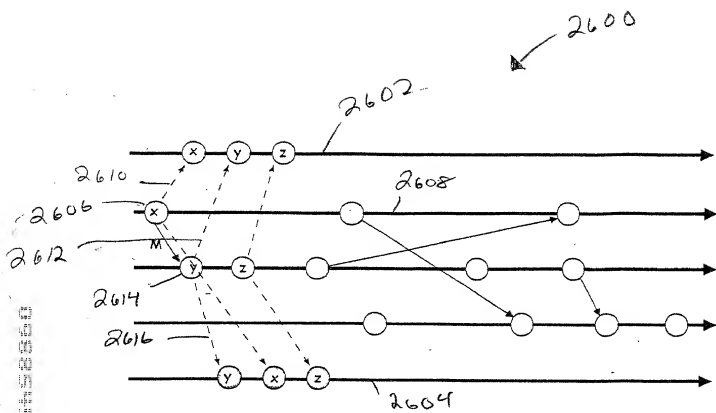


Figure 26

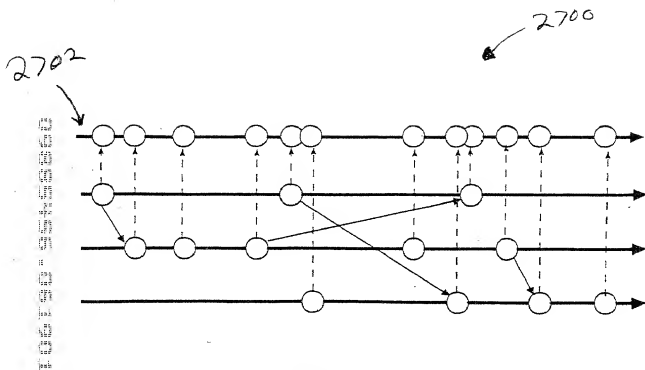


Figure 27

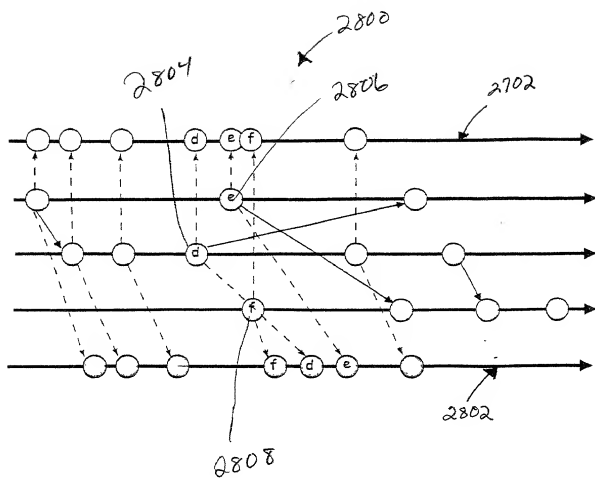


Figure 28

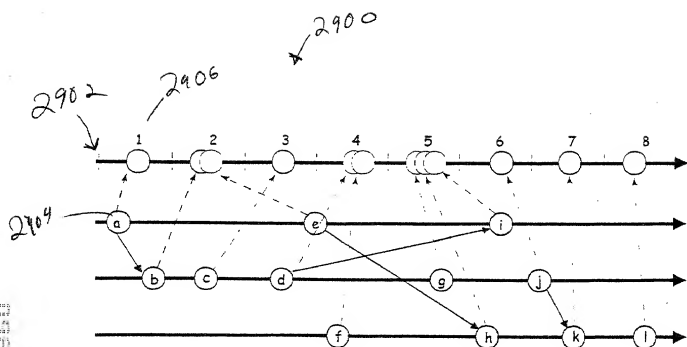


Figure 29

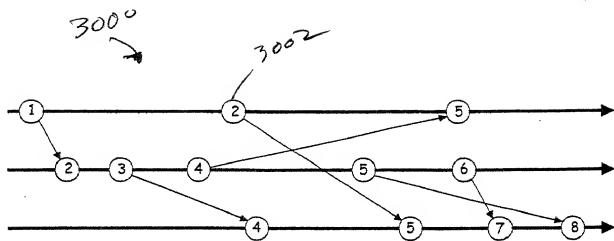


Figure 30

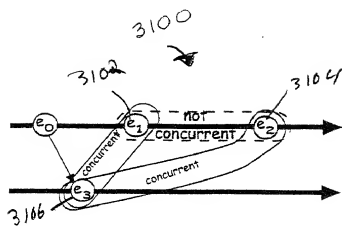


Figure 31

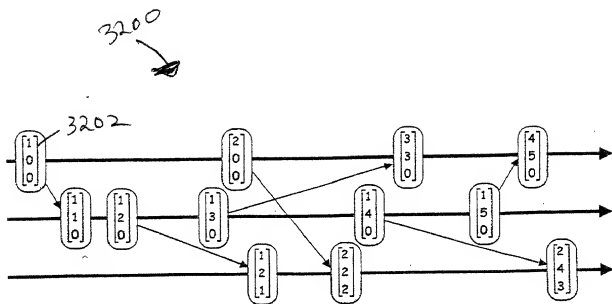


Figure 32

3300

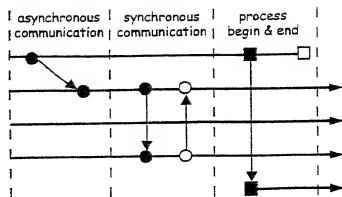


Figure 33

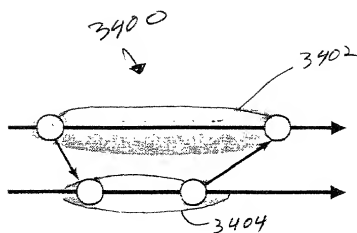


Figure 34

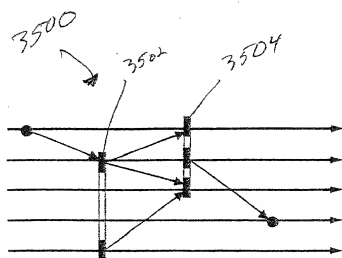


Figure 35

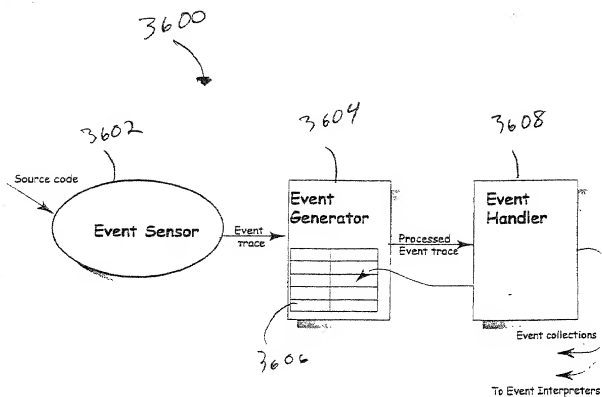


Figure 36

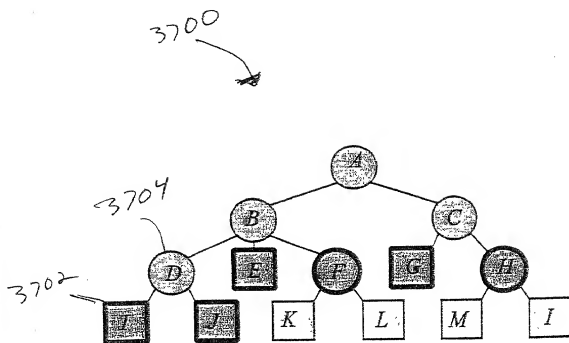


Figure 37

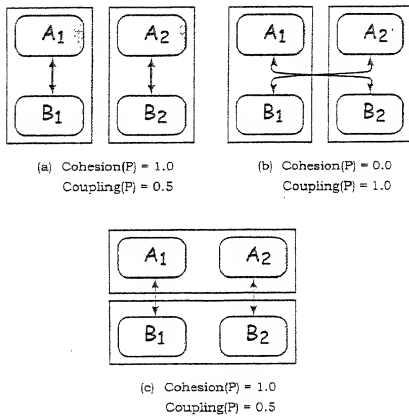


Figure 38

3900

first cut 3902

second cut 3904

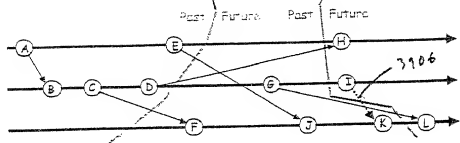


Figure 39

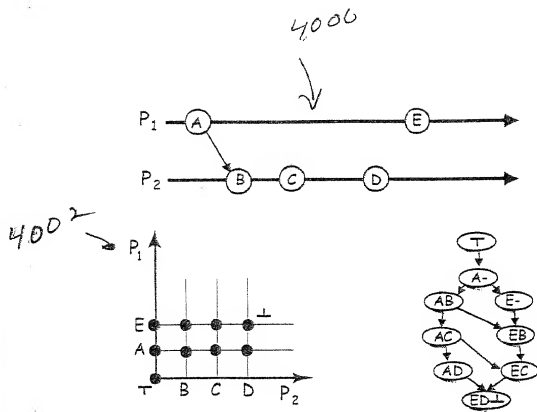


Figure 40

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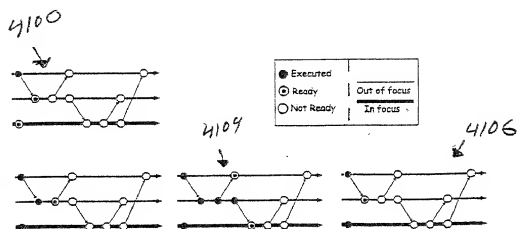


Figure 41

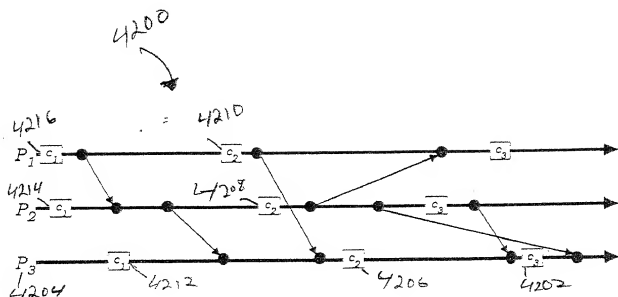


Figure 42

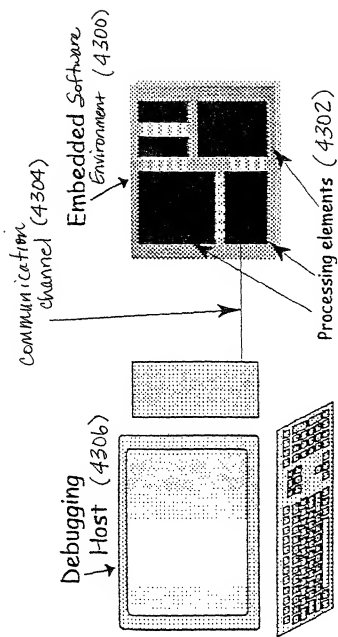


FIG. 43

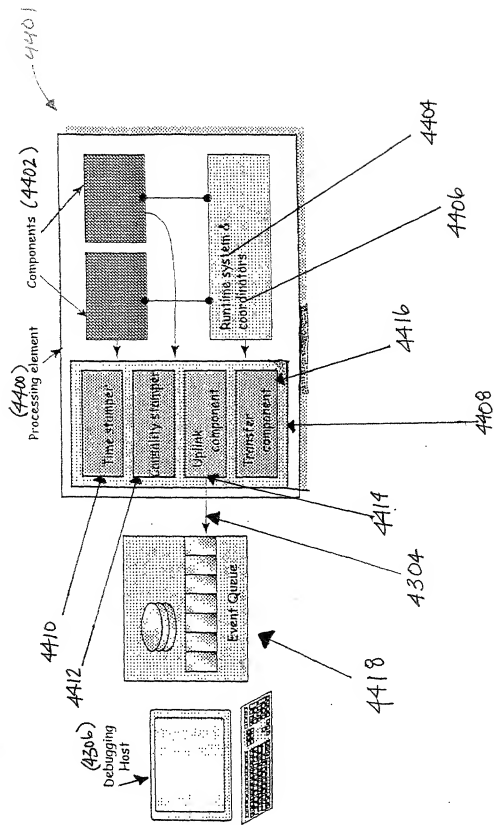


FIG. 44

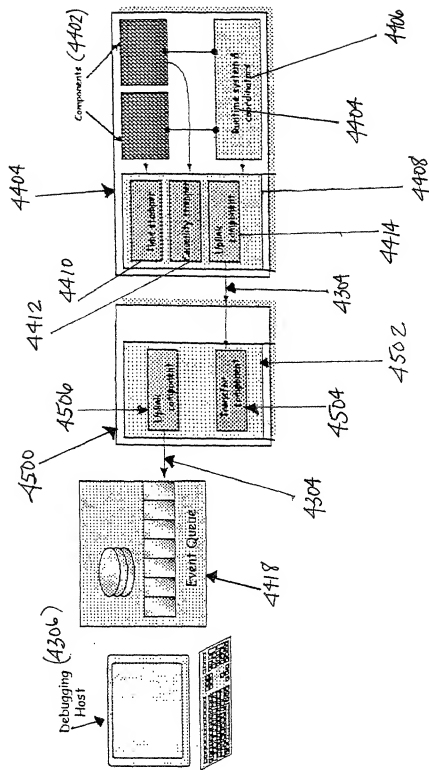


FIG. 45

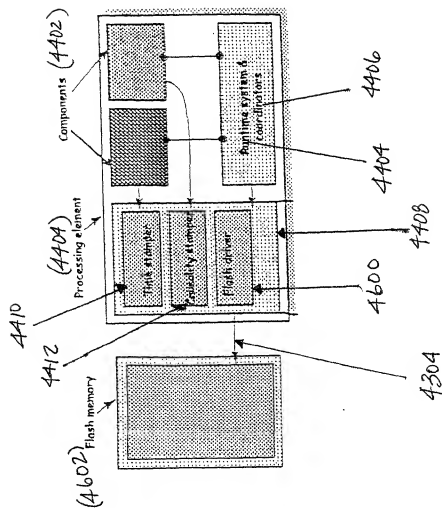
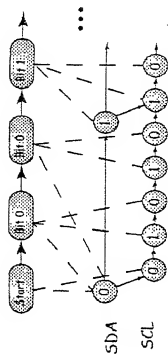


FIG. 46

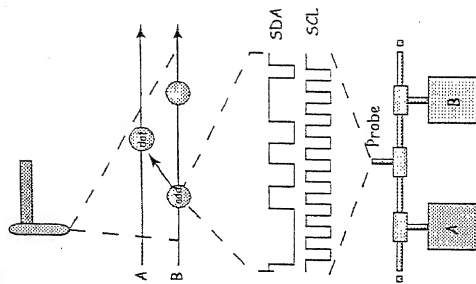
100799 98:50000

Trace 2
Trace 1
snapshot 2
snapshot 1

FIG. 47



Primitive recognition of first two bits



Multiple representations

FIG. 48